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Grace Alicea

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Date: May 17, 2002

OKUDA, et al.

Serial No: 10/018,708

Group Art Unit: *to be assigned*

Filed: Dec. 13, 2001

Examiner: *to be assigned*

For: TABLE OF WAFER POLISHING APPARATUS, METHOD FOR POLISHING SEMICONDUCTOR WAFER, AND METHOD FOR MANUFACTURING SEMICONDUCTOR WAFER

Assistant Commissioner of Patents
Washington, D.C. 20231

TRANSMITTAL LETTER

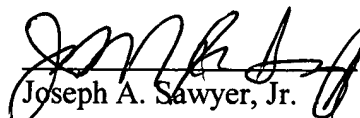
Sir:

In reference to the above-identified application, please find enclosed the following:

- 1) Information Disclosure Statement under 37 CFR 1.97(b);
- 2) PTO Form 1449 and four (4) references;
- 3) Transmittal Letter; and
- 4) Postcard.

If any unresolved issues remain, please contact Applicant's attorney at the telephone number indicated below.

Respectfully submitted,



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